

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q75817

Katsumi ABE

Appln. No.: Unknown

Confirmation No.: Unknown

Group Art Unit: Unknown

Filed: September 18, 2003

Examiner: Unknown

For: DRIVING CIRCUIT AND VOLTAGE GENERATING CIRCUIT AND DISPLAY
USING THE SAME

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith.

1. Japanese Patent Application Publication No. 11-194320, published July 21, 1999.
2. Japanese Patent Application Publication No. 11-194316, published July 21, 1999.
3. T. Nakamura et al., "Low Temperature Poly-Si TFT-LCD with Integrated Analog Circuit", Asia Display/IDW 01 Proceedings, October 16, 2001, pp. 1603-1606 with Abstract.

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4. Y. Mikami et al., "A 5-in.SVGA TFT-LCD with Intergrated Multiple DAC Using Low-Temperature poly-Si TFTs", Asia Display/IDW 01 Proceedings, October 16, 2001, pp. 1607-1610 with Abstract.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date for an application other than a continued prosecution application (CPA) under §1.53(d); (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

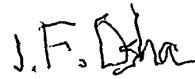
In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant states that above references 1-2 are discussed within the specification beginning at page 2, lines 1 and 2.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

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The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,



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WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: September 18, 2003

Substitute for Form 1449 A & B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	Unknown
				Confirmation Number	Unknown
				Filing Date	September 18, 2003
				First Named Inventor	Katsumi ABE
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket Number	Q75817

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			
		US			
		US			
		US			
		US			
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		US			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	11-194320	A	07/21/1999		
		JP	11-194316	A	07/21/1999		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		T. Nakamura et al., "Low-Temperature Poly-Si TFT-LCD with Integrated Analog Circuit", Asia Display/IDW 01 Proceedings, October 16, 2001, pp. 1603-1606 with Abstract.	
		Y. Mikami et al., "A 5-in. SVGA TFT-LCD with Integrated Multiple DAC Using Low-Temperature poly-Si TFTs", Asia Display/IDW 01 Proceedings, October 16, 2001, pp. 1607-1610 with Abstract.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to indicate here if English language Translation is attached.